MATLAB EXPO 2019

Simplifying Requirements Based Verification with Model-Based Design

Dr. Tjorben Groß, Senior Application Engineer
Key takeaways

- Verify and validate requirements earlier
- Identify inconsistencies in requirements by using unambiguous assessments
- Traceability from requirements to design and test

“By enabling us to analyze requirements quickly, reuse designs from previous products, and eliminate manual coding errors, Model-Based Design has reduced development times and enabled us to shorten schedules to meet the needs of our customers.”

- MyoungSuk Ko, LS Automotive
Challenge: Errors introduced early but found late

Most errors introduced

Unit test finds some errors

Errors found during integration or in field

Requirements → Specification → Hand code
Cost of finding errors increases over time

![Diagram showing the cost of finding errors increasing over time. The graph indicates that the cost of finding errors increases as time progresses.](image-url)
Challenges with requirements-based verification

- Are all requirements implemented?
- Is the implementation functioning correctly?
- How to avoid modifying the design for test?
- Is requirement interpreted correctly?

Requirements → Specification → C/C++ → Hand code
Simulink models for specification

Model-Based Design enables:

- Early testing to increase confidence in your design
- Delivery of higher quality software throughout the workflow

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Multiple languages to describe complex systems

Requirements → Design Model → C/C++ → Hand code
Ad-Hoc Testing: Explore behavior and design alternatives
Validate behavior earlier with simulation
Validate Behavior Earlier with Simulation
Complete Model Based Design

Simulink Models

- Requirements
- Design Model
- Model used for production code generation

Code Generation

Generated code
Systematically verify requirements

- Are all requirements implemented?
- Is the implementation functioning correctly?
- Are designs and requirements consistent?

**Requirements Based Testing**

- **Requirements**
- **Design Model**
- **Simulink Models**
- **Model used for production code generation**
- **C/C++**
- **Generated code**
Integrate with requirements tools and author requirements

- Import from:
  - Word / Excel
  - IBM® Rational® DOORS®
  - ReqIF™ standard

- Update synchronizes changes from source

- Edit and add further details to import

- Author requirements

- Export ReqIF
  - Enables roundtrip with external tools
Roundtrip workflow with external tools thru ReqIF

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- Export ReqIF
  - Enables roundtrip with external tools
Requirements Verification with Simulink

Requirements
- TransmissionReq
  - 1.1 Transmission Operating Modes
  - Reverse cannot be entered from drive
  - Engine only starts in Park

Implemented By
- Test Harness

Verified By
- Simulink / Stateflow

Test Case
- Inputs
  - Signal Editor
  - MAT / Excel file (input)
  - Test Sequence

Assessments
- Test Harness
- Simulink Test
- MATLAB Unit Test
- MATLAB / Excel File (baseline)
- Test Assessments
Requirements Verification with Simulink

Requirements

```
- Driver Switch Request Handling
  - 1.1 Switch precedence
  - 1.2 Avoid repeating commands
```

Implemented By

Test Case

Inputs
- MAT / Excel file (input)
- Signal Editor
- Test Sequence

Verified By

Simulink / Stateflow

Implemented: 16, Justified: 0, None: 2, Total: 18

MATLAB Test
- MATLAB Unit Test
- MATLAB / Excel File (baseline)

Assessments
- Test Assessments
Example: Verifying Heat Pump Controller Requirements

1 Requirements for the basic Heatpump Controller

1.1 Idle when Temperature in Range
If the temperature difference is less than 1 degrees, the system shall be idle with all signals off.

1.2 Activate Fan
The fan shall activate when the temperature difference is greater than or equal to 1 degrees.

1.3 Activate Heat Pump
The pump shall activate when the temperature difference is greater than or equal to 2 degrees for more than 2 seconds and stay active for at least 2 seconds.

1.3.1 Cool Mode
If the room temperature is greater than the set temperature, the system shall cool the space.

1.3.2 Heat Mode
If the room temperature is less than the set temperature, the system shall heat the space.

1.4 Max Temperature
The difference between the room temperature and the set temperature should never exceed 6 degrees.

Requirements in DOORS
Example: Heat Pump Controller Implementation
Link requirements to implementation in model

4: Activate Heat Pump
The pump shall activate when the temperature difference is greater than or equal to 2 degrees for more than 2 seconds and stay active for at least 2

COOLING entry:
fan_cmd = 1;
pump_cmd = 1;
pump_dir = 1;

[sign T(T_req, T_meas) == -1]

HEATING entry:
fan_cmd = 1;
pump_cmd = 1;
pump_dir = -1;

pump_cmd = 0;
pump_dir = 0;

after(2, sec) [mag_T(T_req, T_meas) >= 2]
Work with Model and Requirements with Requirements Perspective

- **Requirement Annotations**
- **Badges**
- **Implementation and Verification Status**
- **Property Inspector**

**Browser**
Isolate Component Under Test with Test Harness

House Heating System

1. Plot temperature of wall, window, and roof (see code)
2. Plot heat flow through wall, window, and roof (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example
Test Sequence Block: Step-based and temporal test sequences
Test Assessments: Formalize and execute requirements

Activate Heat Pump

If the temperature difference exceeds 2 degrees for more than 2 seconds, then the pump shall activate for at least 2 seconds.

When <condition 1> is true,
Then <condition 2> must be true for some time

\[(|x_1 - x_2| \geq x_3)^\varepsilon \land \square_{[0,t_1]}(|x_1 - x_2| \geq x_3) \rightarrow \square_{[0,t_2]} x_4\]

Simple concept

Hard to formalize

MTL logic
Author temporal assessments using form based editor
Execute assessments to verify requirements
Locate implementation of requirement using link
Translate textual requirements into unambiguous Temporal Assessments

- Compose assessments using form based editor
- View assessments as English-like sentence
- Review and debug temporal assessment results
- Link to requirements
Track Implementation and Verification

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<th>Summary</th>
<th>Implemented</th>
<th>Verified</th>
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<tr>
<td>1</td>
<td>#1</td>
<td>Driver Switch Request Handling</td>
<td>Implemented</td>
<td>Passed</td>
</tr>
<tr>
<td>1.1</td>
<td>#2</td>
<td>Switch precedence</td>
<td>Implemented</td>
<td>Passed</td>
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<td>1.2</td>
<td>#3</td>
<td>Avoid repeating commands</td>
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<td>Passed</td>
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<td>1.3</td>
<td>#4</td>
<td>Long Switch recognition</td>
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<tr>
<td>1.4</td>
<td>#7</td>
<td>Cancel Switch Detection</td>
<td>Implemented</td>
<td>Passed</td>
</tr>
<tr>
<td>1.5</td>
<td>#8</td>
<td>Set Switch Detection</td>
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<tr>
<td>1.6</td>
<td>#9</td>
<td>Enable Switch Detection</td>
<td>Implemented</td>
<td>Passed</td>
</tr>
</tbody>
</table>

**Implementation Status**
- Implemented
- Justified
- Missing

**Verification Status**
- Passed
- Failed
- Unexecuted
- Missing
Observers: Separate test/verification logic from design

- Access nested signals without signal lines or changing dynamic response
- Avoid modifying interface for testing
- Simplify design and test by avoiding additional signal lines
Observers: Separate test/verification logic from design

- Access nested signals without signal lines or changing dynamic response
- Avoid modifying interface for testing
- Simplify design and test by avoiding additional signal lines
Re-use tests developed for model to test code

Software in the Loop (SIL)
- Show functional equivalence, model to code
- Execute on desktop

Processor in the Loop (PIL)
- Numerical equivalence, model to target code
- Execute on target board

Hardware in the Loop (HIL)
- Check real-time behavior of the design and code.
- Execute on Speedgoat target computer using Simulink Real-Time
IDNEO Accelerates Development of AUTOSAR Software Components and Complex Device Drivers with Model-Based Design

Challenge
Reduce development time for embedded software for automotive applications

Solution
Use MATLAB and Simulink to model AUTOSAR software components and complex device drivers, run simulation-based tests, and generate embedded C code

Results
- Development time cut by at least 50%
- 80% of errors detected before hardware testing
- Test harnesses and MISRA-compliant C code generated from models

“By using Model-Based Design for all AUTOSAR projects, we have cut development time by at least 50 percent while increasing the number of defects identified early in the design phase and reducing the number of defects found in hardware tests and beyond.”
- Joan Albesa, IDNEO
Summary

- Verify and validate requirements earlier
- Identify inconsistencies in requirements by using unambiguous assessments
- Traceability from requirements to design and test
Learn More

Key products covered in this presentation:

- Simulink Requirements
- Simulink Test
- Embedded Coder
- Simulink Real-Time

Learn more at Verification, Validation and Test Solution Page:
mathworks.com/solutions/verification-validation.html

MathWorks Training Services:
mathworks.com/training-schedule/simulation-based-testing-with-Simulink

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